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Substitute for form 1449/PTO

TWENTIETH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 3

Complete if Known	
Application Number	10/684,348
Filing Date	October 10, 2003
First Named Inventor	Kevin D. KISSELL
Art Unit	2195
Examiner Name	Zhe, Meng Yao
Attorney Docket Number	1778.3310000 (0189.00US)

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	US1	2002/0083173 A1	06-27-2002	Musoll *et al.*	
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	FP2	JP 2007-504536	03-01-2007	Kissell		
	FP3	WO 03/019360 A2	03-06-2003	Armstrong *et al.*		
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	FP5					
	FP6					
	FP7					
	FP8					
	FP9					

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	US41	7,203,823	04-10-2007	Albuz *et al.*	
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	NPL1	AGARWAL, A. *et al.*, "APRIL: A Processor Architecture for Multiprocessing," ACM SIGARCH Computer Architecture News, AMC, Vol. 18, Issue 3a, pp. 104-114, June, 1990.	
	NPL2	CULLER, D.E., *et al.*, Two Fundamental Limits on Dataflow Multiprocessing, 1993, Report No. UCB/CSD 92/716, pgs. 1-14.	
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	NPL5	English Translation of Notice of Rejection for Japanese Patent Application No. 2006-524961, dated November 28, 2008, 3 pages.	
	NPL6	English Abstract of Japanese Patent Publication No. 2007-504536, published September March 1, 2007, 1 page.	
	NPL7	English Abstract of Japanese Patent Publication No. 8-249195, published September 27, 1996, 1 page.	
	NPL8	Exception Handler, from Free Online Dictionary of Computing, 1 page, October 31, 1994.	
	NPL9	Frees, W., "'Teilzeitarbeit Im Prozessor," Electronik, Franzis Verlag Gmbh. Munche, De, Vol. 45, No. 9, 30 April 1996, Pages 100-106, XP000595386, ISSN: 0013-5658.	
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	NPL11	KANE, G. and HEINRICH, J., *MIPS RISC Architecture*, Prentice Hall, Upper Saddle River, New Jersey (1992). (entire book submitted)	
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	NPL15	MIPS32™ Architecture For Programmers, Volume II: The MIPS32TM Instruction Set. Revision 2.00. Pages 231 & 311. Document Number MD00086. June 9, 2003. MIPS Technologies, Inc.	
	NPL16	MIPS32® Architecture for Programmers, Volume III: The MIPS32® Privileged Resource Architecture, Revision 2.50, July 1, 2005, MIPS Technologies, Inc., 137 pages.	
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	NPL22	English Translation of Notice of Rejection for Japanese Patent Application No. 2006-524929, dated November 28, 2008, 4 pages.	
	NPL23		
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	NPL30		

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